



FXO 100GigE

fxo925M100GE-FF

Preliminary product information. Features and technical specifications are subject to change without notice.

General

| | |
|----------------|------------------------------|
| Model | fxo925M100GE-FF |
| Product code | F002464 |
| Product series | FXO 100GigE |
| Status | Prototype/engineering sample |

Sensor

| | |
|--------------------------------|--|
| Sensor type | Area scan |
| Chroma | Mono |
| Spectrum | Visible |
| Spectral range | 350 nm to 1000 nm |
| Resolution | 5,312 × 4,608 (24.50 MP) |
| Sensor model | Sony IMX925 |
| Sensor architecture (material) | cmos |
| Shutter type(s) | global-shutter |
| Sensor size | 14.55 × 12.63 mm (19.27 mm, 19.3mm (Type 1.2)) |
| Pixel size | 2.74 μm × 2.74 μm |

Pixel formats

| | |
|--------------------------|-----------------------------|
| Monochrome pixel formats | mono12, mono12packed, mono8 |
|--------------------------|-----------------------------|

Timing and gain

| | |
|-----------------|-------------------|
| Max. frame rate | 442.7 fps |
| Exposure time | 4 μs to 60 s |
| Gain | 0.0 dB to 48.0 dB |

I/Os and power

| | |
|-----------------------------|---|
| Non-isolated lines | 0 x LVDS input, 0 x LVDS output, 0 x TTL input, 0 x TTL output, 2 x 24V input, 2 x Open drain output, |
| Specific non-isolated lines | 0 x RS232 input, 0 x RS232 output, 1 x RS422 input, 1 x RS422 output, |
| Opto-isolated lines | 0 x Optical isolated input, 0 x Optical isolated input, |
| Power supply | 24 to 25VDC |
| Power consumption | External: 26 W (typical) |

Operating conditions

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|---------------------------------|-----------------|
| Operating temperature (housing) | -10 °C to 60 °C |
|---------------------------------|-----------------|

Mechanical properties

| | |
|-----------------------------------|-----------------------------|
| Body dimensions (L x W x H in mm) | 92 × 58 × 58 |
| Filter/protection glass | K9 - AR coating - 400-850nm |
| IP class | IP30 |
| Lens mount(s) | Flat Front |
| Weight | 450 g |

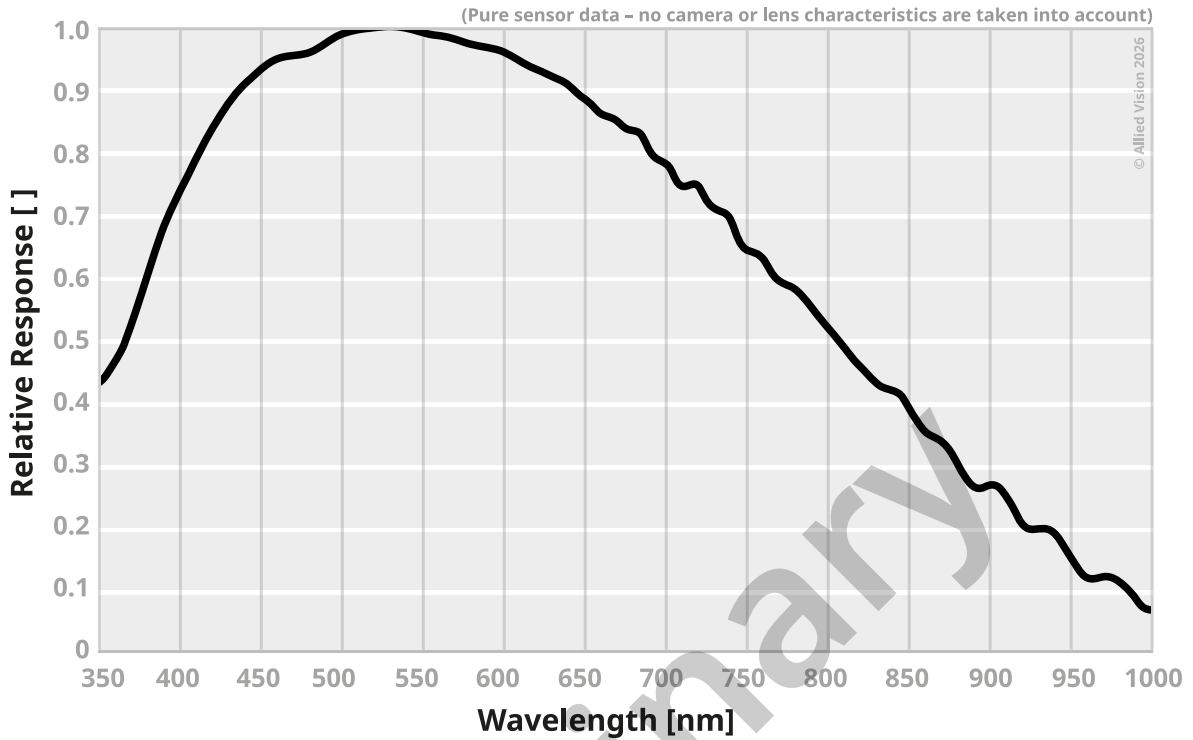
On-board memory and FPGA

| | |
|-----------------------------|-------------|
| Image buffer (RAM) | 4032 MByte |
| Non-volatile memory (Flash) | 32768 MByte |

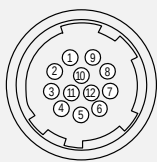
Interfaces

| | |
|---------------------|----------|
| Digital interface | 100gige |
| Interface connector | (QSFP28) |

Quantum Efficiency



I/O pin assignment



| | |
|----|--------------------|
| 1 | VIn- (GND) |
| 2 | VIn+ (24 VDC) |
| 3 | In 4 (Rx D RS232) |
| 4 | Out 4 (Tx D RS232) |
| 5 | In 1 (0 ... 24 V) |
| 6 | In 2 (0 ... 24 V) |
| 7 | Out 1 (open drain) |
| 8 | Out 2 (open drain) |
| 9 | In 3+ (opto In+) |
| 10 | In 3- (opto In-) |
| 11 | Out 3 (open drain) |
| 12 | Out 0 (open drain) |